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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:  
Ming-Fang Wang, et al.

Serial No.: 10/696,007

Filed: October 29, 2003

For: PROCESS FOR INTEGRATION OF  
A HIGH DIELECTRIC CONSTANT  
GATE INSULATOR LAYER IN A  
CMOS DEVICE

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Attorney Docket No.  
2001.0808B / 24061.374

Customer No. 42717

Group Art Unit: 2822

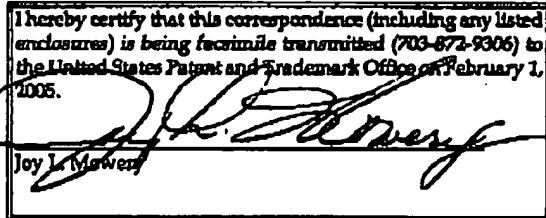
Examiner: Mark V. Prenty

Confirmation No.: 7109

OK  
to  
enter  
MP  
2/05

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:



AMENDMENT UNDER 37 CFR 61.111

This Amendment is submitted in reply to the Office Action mailed on December 3, 2004.  
(Please see the pages that follow).